

Patent



Attorney Docket No.: 2207/353102

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Viktor Andrew TYMCHENKO

SERIAL NO. : 09/955,007

FILED : September 10, 2001

PATENT NO. : 6,909,922 B2

ISSUED : June 21, 2005

FOR : APPARATUS, METHOD AND COMPUTER SYSTEM FOR
REDUCING POWER CONSUMPTION OF A PROCESSOR
OR PROCESSORS UPON OCCURRENCE OF A FAILURE
CONDITION AFFECTING THE PROCESSOR OR
PROCESSORS

GROUP ART UNIT : 2121

EXAMINER : Emanuel Todd VOELTZ

**Certificate
SEP 23 2005
of Correction**

M/S: CERTIFICATE OF
CORRECTION BRANCH
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: CERTIFICATE OF CORRECTION BRANCH, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: <u>September 14, 2005</u>	Signature: <u>Pilar Rodriguez</u> Pilar Rodriguez

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

We have compared the above patent with the application as filed and have found errors in the printing of the patent. We respectfully request that the enclosed Certificate of Correction on Form PTO-1050 be issued correcting the mistakes set forth therein under authority of 35 U.S.C.

SEP 23 2005

§254. The exact column and line number where the errors occurred in the patent are listed on the enclosed certificate.

The errors that appear in the patent are various typographical errors made by the Patent Office and no fee is believed required.

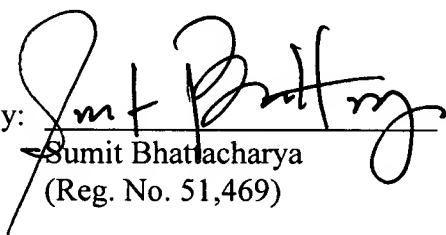
The Office is hereby authorized to charge any additional fees, or credit any overpayments, to Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON

Dated: September 14, 2005

By:


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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 6,909,922 B2
DATED : June 21, 2005
INVENTOR(S) : Viktor Andrew TYMCHENKO

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, Line 42 "external input asserted."
should be --external input is asserted.--

Column 10, Line 5 "powerreduction circuit"
should be --power reduction circuit"

MAILING ADDRESS OF SENDER: Attorney Ref.: 2207/353102
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Date: Sept. 14, 2005

PATENT NO. 6,909,902 B2

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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